

Search Notes

Application/Control No.

10/767,001

Applicant(s)/Patent under
Reexamination

ZHANG ET AL.

Examiner

Art Unit

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SEARCHED

Class	Subclass	Date	Examiner
710	113 305	12/8/2005	CEL
711	122 133	12/8/2005	CEL
711	154	12/8/2005	CEL
370	452	12/8/2005	CEL
Above	UPDATED	3/13/2006	CEL
Above	UPDATED	10/28/2006	CEL
Above	UPDATED	3/14/2007	CEL
710	52 110	6/18/2007	CEL
710	113 240	6/18/2007	CEL
710	309 310	6/18/2007	CEL
710	313	6/18/2007	CEL
713	600	6/18/2007	CEL

INTERFERENCE SEARCHED

Class	Subclass	Date	Examiner
710	309 310	6/28/2007	CEL
710	313	6/28/2007	CEL
370	229	6/28/2007	CEL
PGPub text search See interference search history printout.		6/28/2007	CEL

**SEARCH NOTES
(INCLUDING SEARCH STRATEGY)**

	DATE	EXMR
EAST - Data Transferring Control with Buffering scheme, Caching latency reducing technique	12/8/2005	CEL
Search Updated	3/13/2006	CEL
Case Reopen after Appeal	10/26/2006	CEL
EAST [USPT, JPO, EPO, IBM_TDB] Buffer Bypassing controller reducing data transfer latency See Search History printout	10/28/2006	CEL
Search Updated	3/14/2007	CEL
EAST [USPT, JPO, EPO] buffer bypassing controller regarding to operation clock cycles See Search History printout	6/18/2007	CEL